# **Electronic Power Conversion**

**Power Semiconductors** 

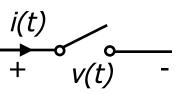


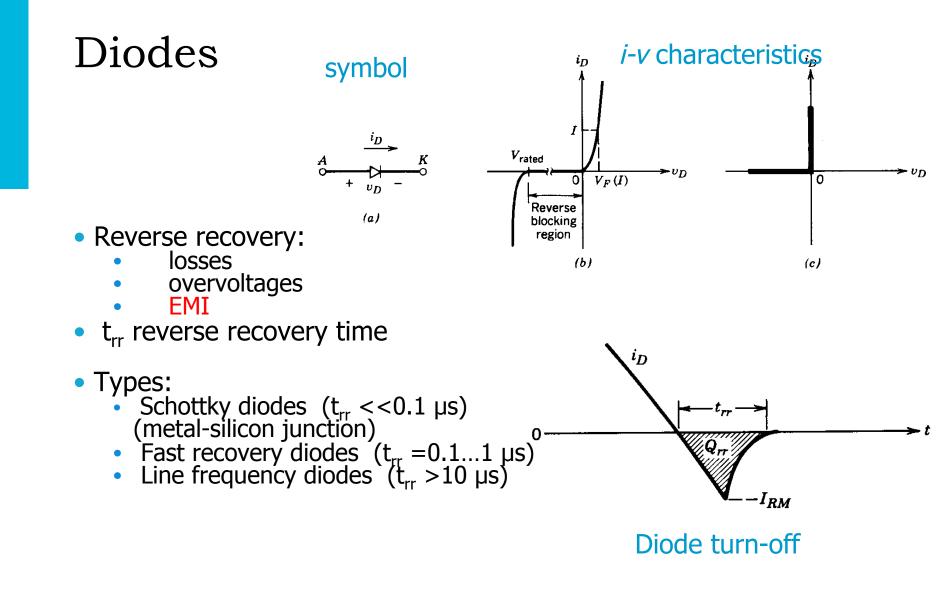
# 2. Overview of Power Semiconductor Devices

- Ideal switch:
  - Off *i(t)* =0
  - On *v(t)*=0
  - Power loss  $p(t)=v(t)i(t)=0 \rightarrow \text{lossless device}$
- Use of idealized switches in analysing circuits is justified if:
  - On-state voltage << supply voltage</li>
  - Switch transition time << switching period</li>
- Classification according to controlability:
  - Diodes (uncontrollable)
  - Thyristors (half controllable)
  - Fully controllable switches

reverse blocking = *sperren* forward blocking = *blokkeren* on-state = *doorlaten* 

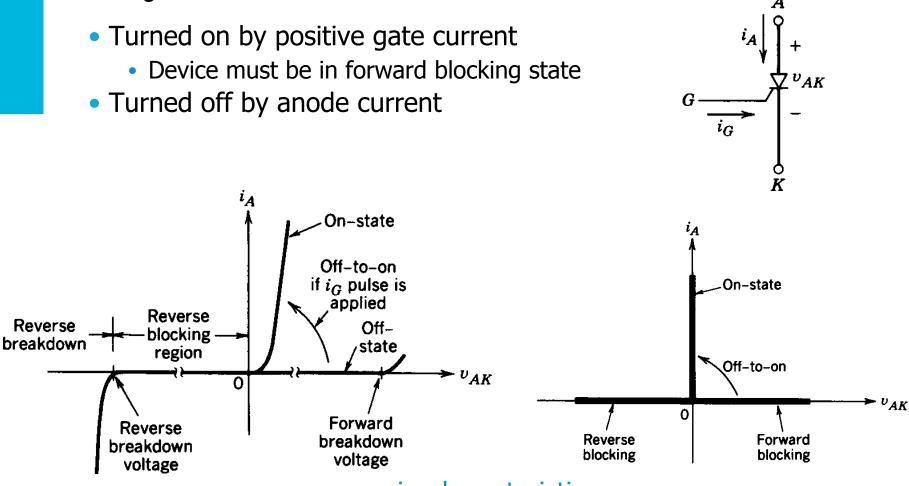








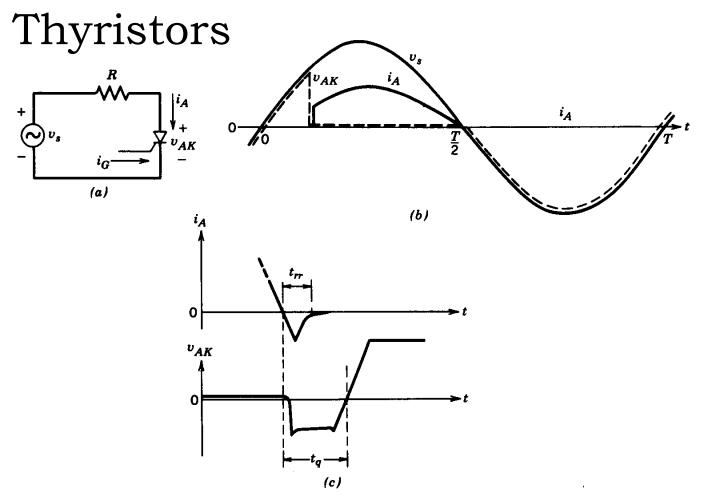
# Thyristors



*i-v* characteristics



symbol



- t<sub>rr</sub> reverse recovery time (*hersteltijd*)
- t<sub>q</sub> circuit commutation time (circuit commutatietijd)

**T**UDelft

Note: Fig. c) does not concern a detail of fig. b); it concerns another case

# Controllable Switches

- Desired properties
  - High forward and reverse blocking voltage at low leakage current
  - High current carrying capability at low forward voltage drop
  - Low drive power
  - Fast switching  $\rightarrow$  low switching loss
- BJT, MOSFET, GTO, IGBT, IGCT, ...

$$i(t)$$
  
+  $v(t)$  -



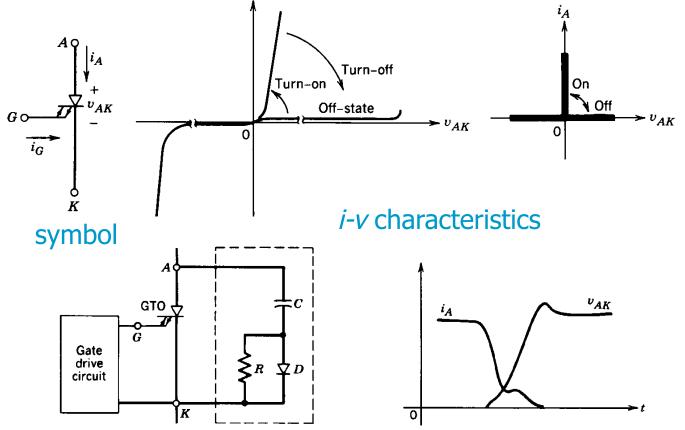
#### Power Semiconductor Device Losses control signal On Ideal- $I_o$ Off Off $t_{\rm off}$ $V_d$ $T_s = \frac{1}{L}$ $i_T$ VT, iT $V_d$ $V_d$ $I_0$ $W_{c.on} = \frac{1}{2} V_d I_o t_{c.on}$ Von td(off) tru t<sub>ri</sub> | t<sub>fv</sub> 0 Turn-on energy t<sub>d(on)</sub> $p_T(t)$ c(0n) · c(off) $W_{c.off} = \frac{1}{2} V_d I_o t_{c.off}$ $V_d I_o$ $W_{c(off)} \simeq \frac{1}{2} V_d I_o t_{c(off)}$ $W_{c(\text{on})} \simeq \frac{1}{2} V_d I_o t_{c(\text{on})}$ Turn-off energy 0 $P_s = \frac{1}{2} V_d I_o f_s \left( t_{c.on} + t_{c.off} \right)$ $\frac{t_{on}}{T_{a}}$ $P_{on} = V_{on} I_o$ Conduction loss Switching loss

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# GTO-thyristor (Gate Turn Off thyristor)

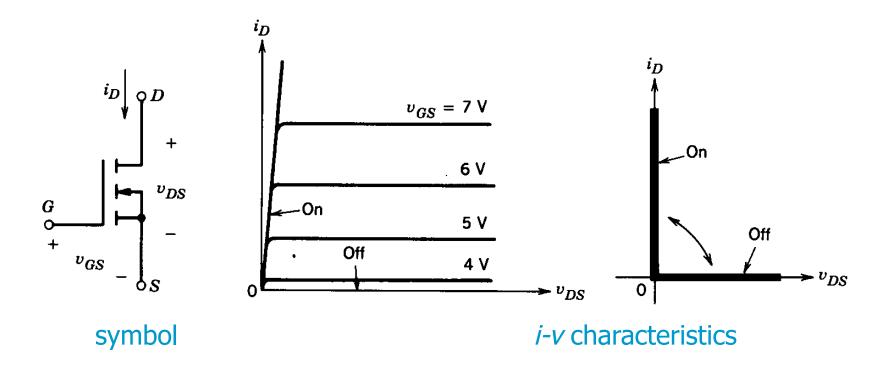
- Can be turned-off by negative gate current
- Snubber needed to reduce dv/dt at turn-off





# MOSFET

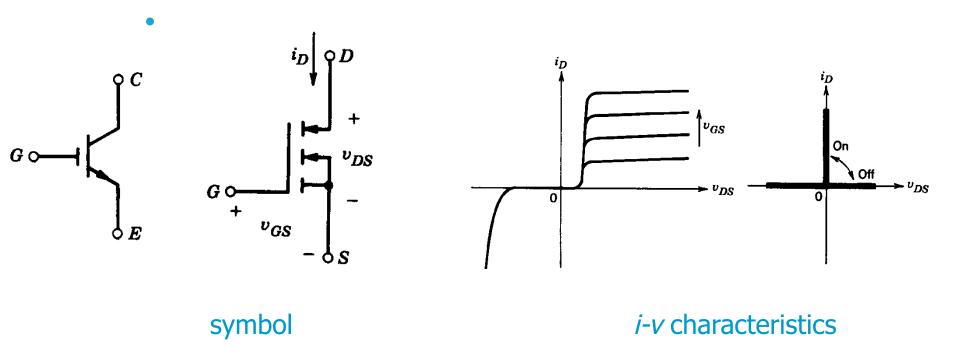
- Voltage controlled device;
- Fast, switching times ~ns to ~a few hundred ns;
- On-resistance increases exponentially with blocking voltage rating



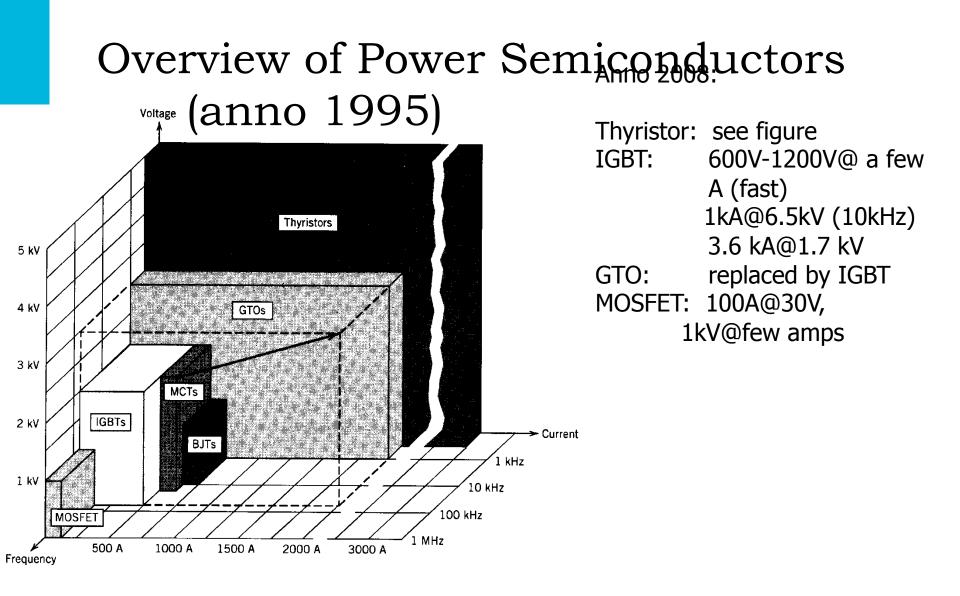


# IGBT (Insulated Gate Bipolar Transistor)

- High impedance gate (like MOSFET);
- Low on-state voltage (like BJT);
- Switching times ~1µs (new devices are faster).

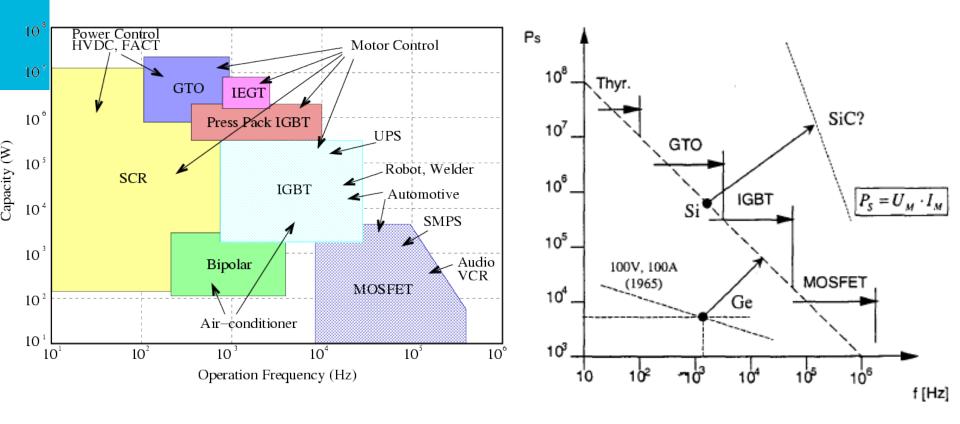








### **Overview of Power Semiconductors**



Source: van Wyk, "Power electronics technology at the dawn of the new milleniumstatus and future", IEEE PESC 1999.



#### Image credits

- All uncredited diagrams are from the book "Power Electronics: Converters, Applications, and Design" by N. Mohan, T.M. Undeland and W.P. Robbins.
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